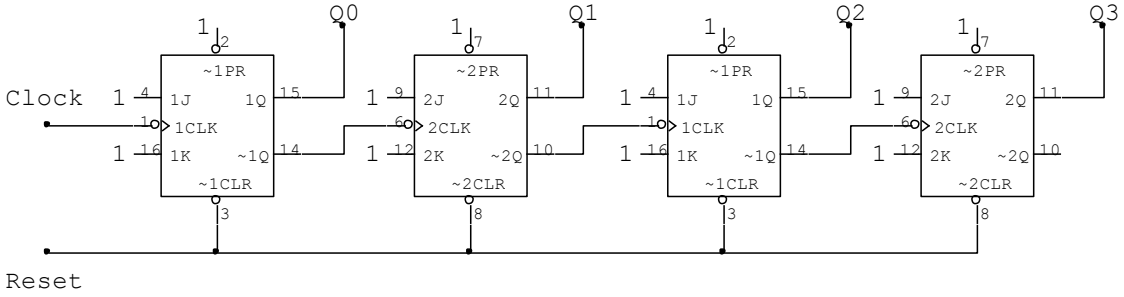
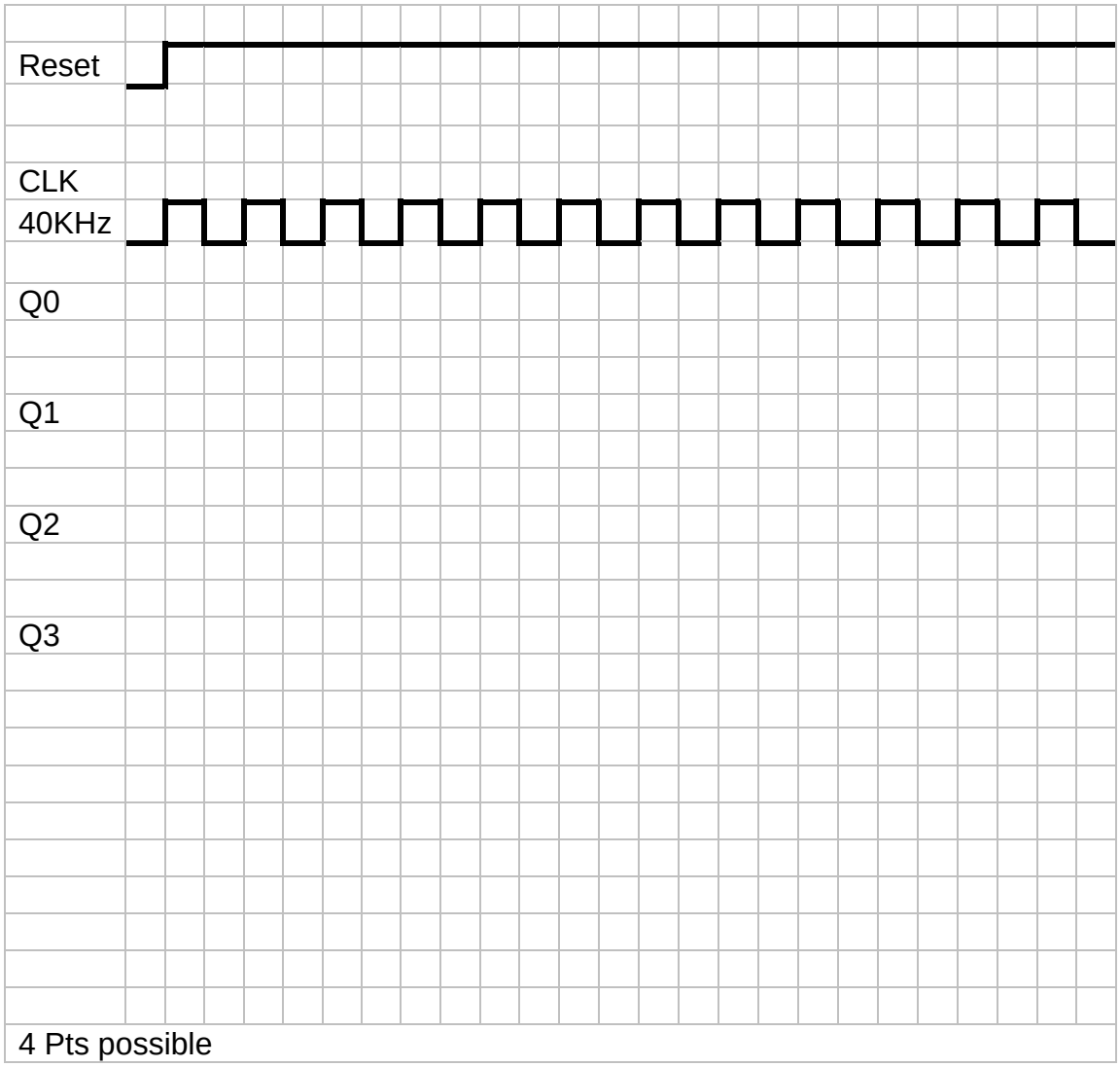
Digital 2 Test 3 Review

NAME: _____





Digital 2 Test 3 Review

The counter on the previous page is designed to: (CIRCLE ONE)

COUNT UP COUNT DOWN

It is also classed as a: (CIRCLE ONE)

RIPPLE COUNTER SYNCHRONOUS COUNTER

It is a mod _____ counter. (FILL IN THE BLANK)

Which output is the MSB? (CIRCLE ONE)

Q0 Q1 Q2 Q3

What is the frequency at each of the outputs if the input clock frequency is 40 KHz? (FILL IN THE BLANKS)

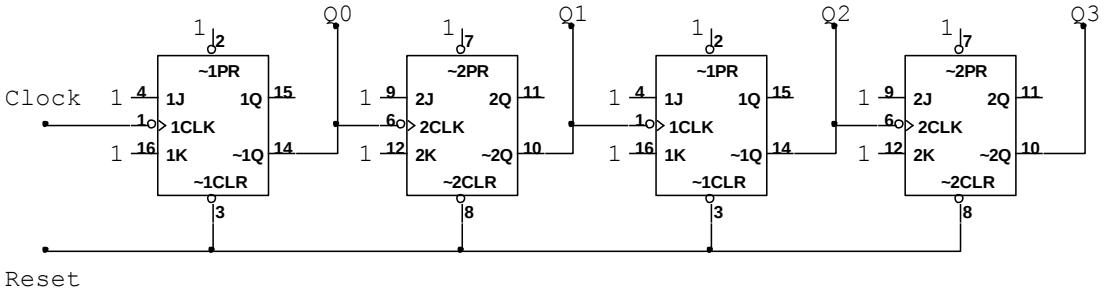
Q0 = _____

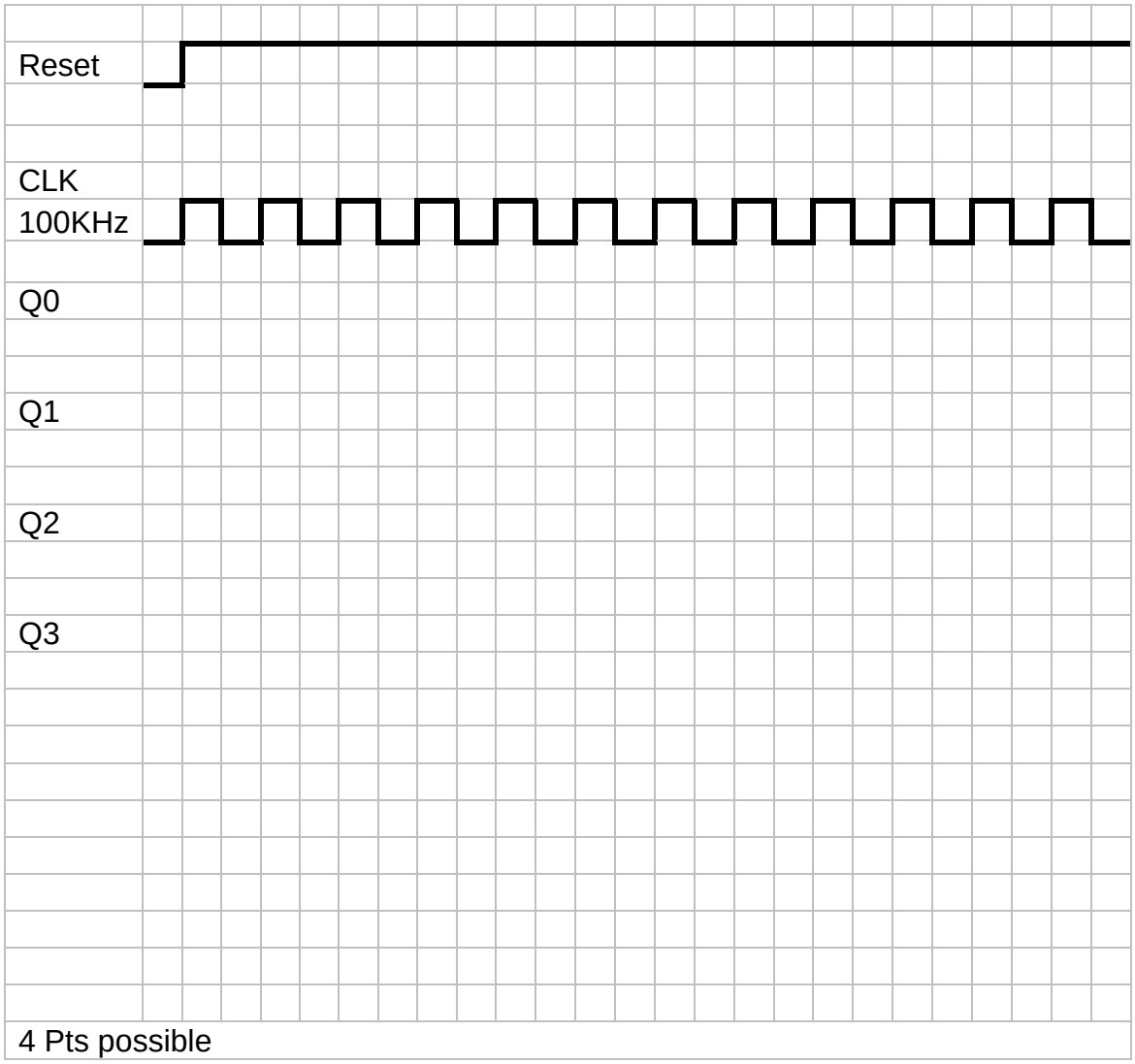
Q1 = _____

Q2 = _____

Q3 = _____

Digital 2 Test 3 Review





The counter on the previous page is designed to: (CIRCLE ONE)

COUNT UP COUNT DOWN

It is also classed as a: (CIRCLE ONE)

RIPPLE COUNTER SYNCHRONOUS COUNTER

It is a mod _____ counter. (FILL IN THE BLANK)

Which output is the MSB? (CIRCLE ONE)

Q0 Q1 Q2 Q3

What is the frequency at each of the outputs if the input clock frequency is 100 KHz? (FILL IN THE BLANKS)

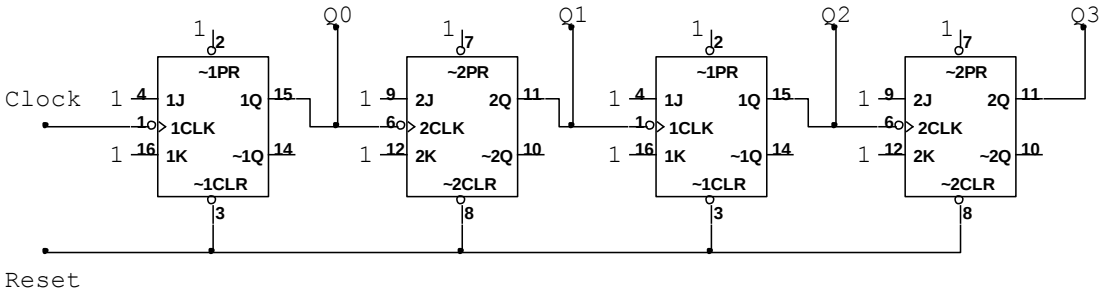
Q0 = _____

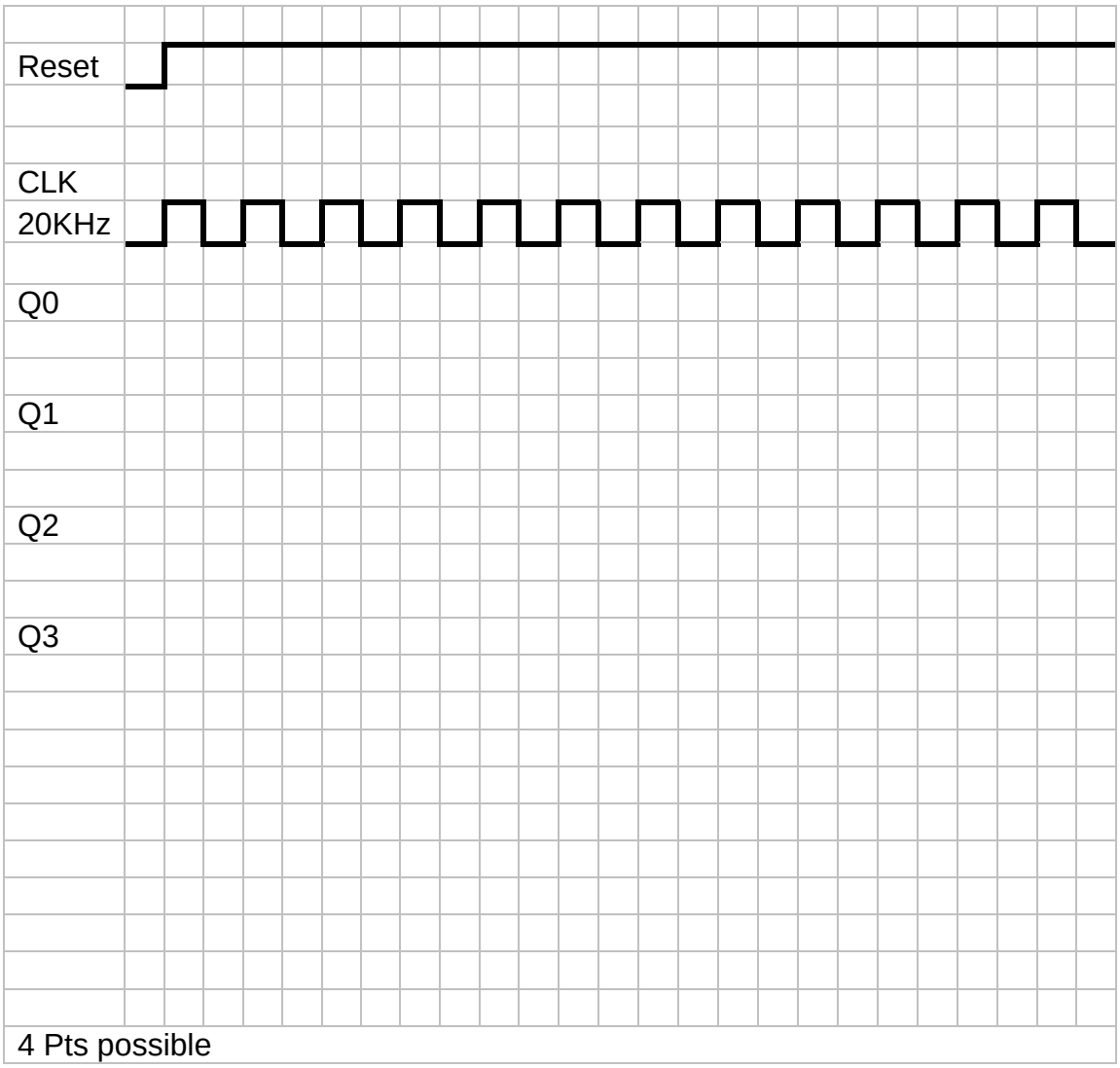
Q1 = _____

Q2 = _____

Q3 = _____

Digital 2 Test 3 Review





The counter on the previous page is designed to: (CIRCLE ONE)

COUNT UP COUNT DOWN

It is also classed as a: (CIRCLE ONE)

RIPPLE COUNTER SYNCHRONOUS COUNTER

It is a mod _____ counter. (FILL IN THE BLANK)

Which output is the MSB? (CIRCLE ONE)

Q0 Q1 Q2 Q3

What is the frequency at each of the outputs if the input clock frequency is 20 KHz? (FILL IN THE BLANKS)

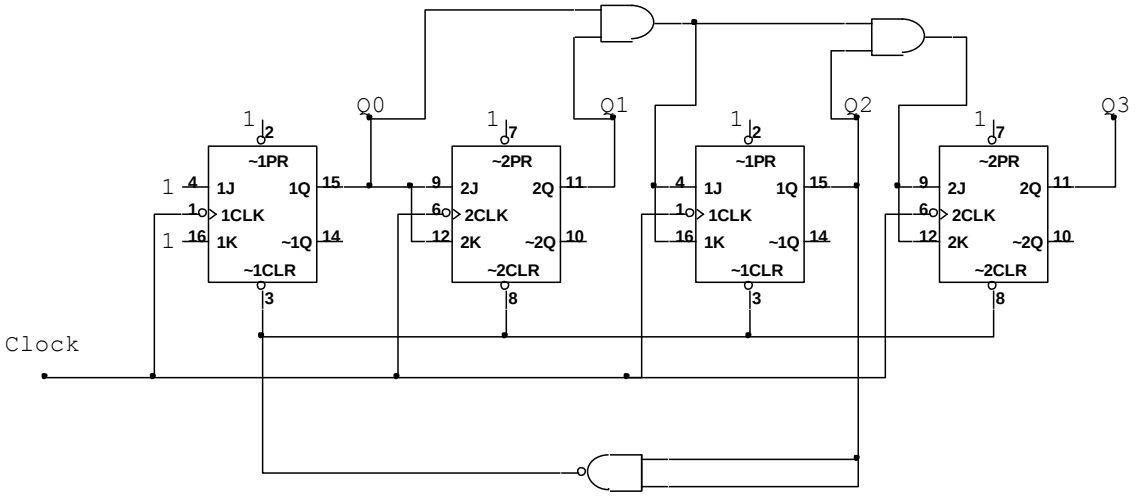
Q0 = _____

Q1 = _____

Q2 = _____

Q3 = _____

Digital 2 Test 3 Review



On which count does this counter reset? (FILL IN THE BLANK) _____

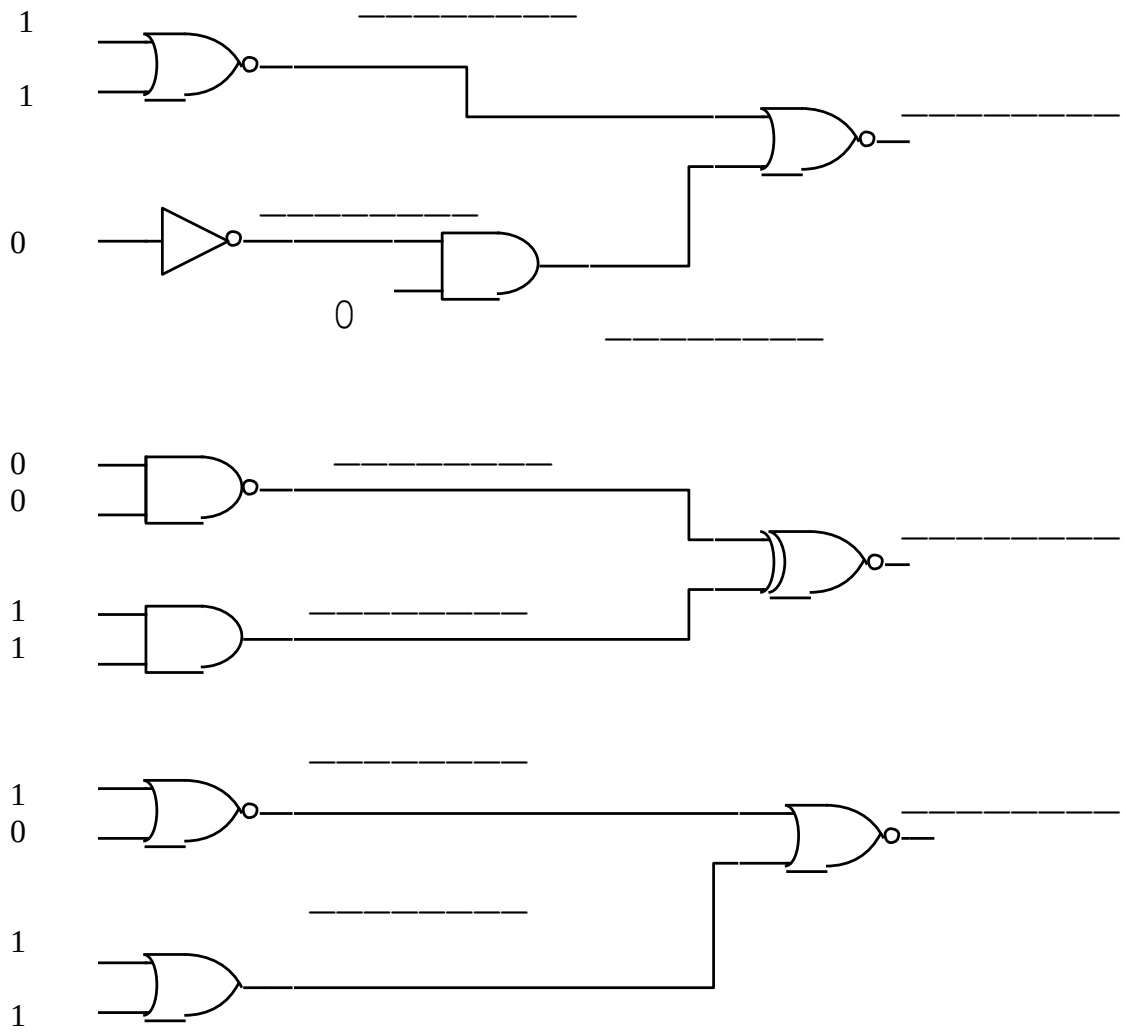
This counter is a: (CIRCLE ONE)

Ripple Counter Synchronous Counter

This counter is a mod _____. (FILL IN THE BLANK)

This counter will: (CIRCLE ONE)

Count up Count down

Digital 2 Test 3 Review

Fill in the blanks with the output of each gate with a 1 or 0
10 Pts possible